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A3602-US-NP

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In re Application of William S. Wong et al.

Group Art Unit: 2811

Application No.: 10/734,429

Examiner:

Filed: December 12, 2003

Confirmation No.: 3875

For: THIN FILM TRANSISTOR ARRAY WITH RING GEOMETRY

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:


SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) and information listed on the attached PTO-1449 Form. One legible copy of each is attached to the PTO-1449 Form, except for U.S. Patents, or published or IFW (Image File Wrapper) available U.S. applications. It is respectfully requested that the references and information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of the above application (excluding an RCE), **OR** (b) before the mailing date of a first Office Action on the merits (including an RCE). *No certification or fee is required.*
- ☐ 2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date **AND** after the date of the first Office Action on the merits, but before the mailing date of a Final Rejection, Notice of Allowance, or other action that closes prosecution. Please debit Xerox Corporation Deposit Account 24-0025 in the amount of \$180.00 in payment of the fee under 37 C.F.R. 1.17(p). *(A copy of this paper is attached.)*
- ☐ 3. This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance; **AND**
- ☐ a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(1); **(OR)**

Application No. 10/734,429

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- ☐ 5. Per 37 C.F.R. 1.98(a)(3), a concise explanation of the relevance of each of any submitted item that is NOT in the English language is either enclosed herewith, or incorporated in the application specification.
- ☐ 6. A copy of an English language version of an above-referenced counterpart foreign application search report is attached.
- ☐ 7. Copies of some or all of the subject references were cited or submitted to the Office in related parent Application No. _____, filed _____, which is relied upon under 35 U.S.C. §120. Thus, copies of those references are not attached (except for copies of cited pending applications). 37 C.F.R. §1.98(d).



Kent M. Chen

Signature under 37 CFR 1.33 & 34

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Date March 15, 2005

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PTO/SB/08A (08-03)

Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/734,429
Filing Date	December 12, 2003
First Named Inventor	William S. Wong
Art Unit	2811
Examiner Name	
Attorney Docket Number	A3602-US-NP

Sheet 1 of 1

Non Patent Literature Documents

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		H. KLAUK, D. J. GUNDLACH, T. N. JACKSON: Sub-75- μ sec Pentacene Organic TFT Circuits, 55 th Annual Device Research Conference Late News Digest, June 1998	<input checked="" type="checkbox"/>
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